

08-29-01

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August 28, 2001

11046 U.S. PTO
09/941144
08/28/01

Commissioner of Patents and Trademarks
U.S. Patent and Trademark Office
Washington, DC 20231

RE: New US Patent Application Entitled: METHOD OF SCALABLE NON-BLOCKING SHARED MEMORY OUTPUT-BUFFERED SWITCHING OF VARIABLE LENGTH DATA PACKETS FROM PLURALITIES OF PORTS AT FULL LINE RATE, AND APPARATUS THEREFOR
Inventors: Xiaolin Wang, Satish Soman, Subhasis Pal
Assignment to: Axiowave Networks, Inc.

Dear Sir:

We enclose the above-identified patent application, the filing fee of \$391.00, (small entity) together with 2 (two) sheets of drawings, and the duly executed Declaration (power of attorney).

The Commissioner is hereby authorized to charge any additional fees that may be due, to the deposit account No. 18-1425 of the undersigned attorney,

Cordially,

RINES AND RINES

Robert H. Rines

Robert H. Rines

RHR/ipo--Enclosures

CERTIFICATE OF SERVICE

I, the undersigned, hereby certify that I caused to be mailed by the United States Express Mail Service, No. EL669465040US, under date of August 28, 2001 postage pre-paid, a new US patent application entitled: METHOD OF SCALABLE NON-BLOCKING SHARED MEMORY OUTPUT-BUFFERED SWITCHING OF VARIABLE LENGTH DATA PACKETS FROM PLURALITIES OF PORTS AT FULL LINE RATE, AND APPARATUS THEREFOR

Inventors: Xiaolin Wang, Satish Soman, Subhasis Pal

Assignment to: Axiowave Networks, Inc



Irene O'Mara

Date: August 28, 2001

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